UNH-IOL — 21 Madbury Road, Suite 100 — Durham, NH 03824 — +1-603-862-0090

Carly Sun 10Gtek Transceivers Co. Ltd info@10gtek.com March 15, 2018

Enclosed are the results from the Open Network Systems Interoperability performed on:

Module or Cable Assembly Under Test (MUT/CUT):

Vendor and Device Type	10Gtek QSFP
Part Number	AOC-Q4Q4- 010

Host Under Test (HUT):

Host System 1 Composition				
Network Operating System	Cumulus			
OS Version	3.5.2			
Bare Metal Switch	Wedge 100			
Part Number	100			
ONIE Version	2018.02			

This testing pertains to the Open Network Systems Interoperability Test Plan, which outlines a series of tests performed on a variety of optical transceivers and cables with bare-metal open switches running Network Operating Systems from multiple vendors. The focus of these tests was basic interoperability, which aims to validate the operation of open network systems.

As always, we welcome any comments regarding this Test Suite. If you have any questions about the test procedures or results, please feel free to contact me via e-mail at david@iol.unh.edu or by phone at +1-603-862-0090.

Regards, David Woolf

Digital Signature Information

This document was created using an Adobe digital signature. A digital signature helps to ensure the authenticity of the document, but only in this digital format. For information on how to verify this document's integrity proceed to the following site:

https://www.iol.unh.edu/testing/reports/certificate-install

If the document status still indicates "Validity of author NOT confirmed", then please contact the UNH-IOL to confirm the document's authenticity. To further validate the certificate integrity, Adobe 6.0 should report the following fingerprint information:

MD5 Fingerprint: 80 60 3C EA 42 D6 61 38 62 24 14 6A 1F 66 E9 84 SHA-1 Fingerprint: 81 FF 90 E8 56 CB 95 7F 3E D6 4D B8 B2 99 EF BE 3C CC 7D DE



In section 2, the following equipment was used:

Test System Hardware	
EEPROM Reader	I2C Elite Reader

In section 3, the following equipment was used:

Test System Hardware	
Network Analyzer	PNA –Performance Network Analyzer

In section 4, the following equipment was used:

Test System Hardware					
Wide Bandwidth	Keysight DCA-X 86100D Wide Bandwidth Oscilloscope				
Oscilloscope					
Waveform Analyzer	Keysight 86105C Optical Waveform Analyzer				
50GHz Waveform	Keysight 86108B Mega Module, 50GHz Bandwidth Waveform				
Analyzer	Analyzer				
Clock Recovery Module	Keysight 83496B Optical/Electrical Clock Data Recovery Unit				
High Performance Serial BERT	Tektronix BERTScope				
Signal Quality Analyzer	BERTScope should be set up to transmit PRBS9 at 10.3125Gbps				
De-Emphasis Signal	Agilent N4916B				
Converter					

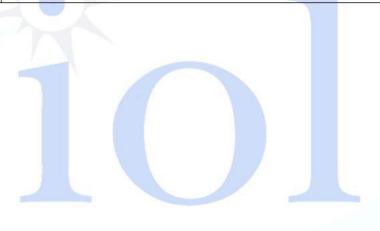
^{*}Two modules used during testing

In section 6, an MLNX NICS was used to generate test traffic:

Test System Hardware					
Ethernet Traffic Generator	MLNX NICS				
Software Version	Ostinato				
Port Types	40/100G				

The following table contains possible results and their meanings.

Result	Interpretation				
PASS	The Device Under Test (DUT) was observed to exhibit conformant behavior.				
PASS W/ Comments	The specified behavior is demonstrated by the DUT; however this result indicates that either changes were made to the standard test procedure or results other than the expected results were observed.				
FAIL	The DUT was observed to exhibit non-compliant behavior.				
INFO	This Test is designed for informational purposes only. While the results may help ensure the interoperability of the DUT, a PASS/FAIL is not given for this test.				
WARN	The DUT was observed to exhibit behavior that is not recommended.				
N/A	Not Applicable. This test is not applicable for the DUT.				
N/S	Not Supported. This test was not run due to features not implemented on the DUT.				
N/T	Not tested. This test was not run.				



Summary of Results- Conformance				
Test	Result			
Test 1.1:	N/A			
Test 1.2:	PASS			
Test 3.1.1: Return Loss for 10G Passive Cable	PASS			
Test 3.1.2: Insertion Loss for 10G Passive Cable	PASS			
Test 5.1.1: Output Rise and Fall Times for 100G Host	PASS			
Test 5.1.2: Transmitter Eye Mask for 100G Host	PASS			
Test 5.1.3: Total Jitter for 100G Host	PASS			
Test 5.1.4: Input and Output Return Loss on 100G Host	PASS			

Summary of Results - Interoperability					
Test	Result				
Test 2.1: Physical Compatibility with Supporting Devices	PASS				
Test 2.2: Host Management of Module or Cable Assembly	PASS				
Test 2.3: Diagnostic Optical Monitor Support	PASS				
Test 6.1: Establish Baseline Performance Analysis	PASS				
Test 6.2:	PASS				
Test 6.3:Packet Error Rate Estimation	PASS				
Test 6.4: Packet Loss/Stress Test	PASS				

Notes						

Test Information	Test Result
Test 1.1:	
<i>Purpose</i> : To verify that a host can pass a random sampling of ONIE Compliance Environment tests.	N/A
Comments on Test Procedure	21
The random sampling of tests from the ONIE Compliance Environment was not used. Comments on Test Results	
Additional Comments	
This test is only applicable to Hosts which have not performed ONIE compliance previously.	ance testing

Test Information	Test Result
Test 1.2:	PASS
Purpose: To verify that a NOS can be successfully installed through ONIE.	TASS
Comments on Test Procedure	
This test was completed using the standard procedure as written in the Test Pl the ONIE Compliance Environment was not used. Comments on Test Results	an. The random sampling of tests from
Comments on Test Results	
Part A: The DUT was able to install the NOS via ONIE. Part B: The DUT was able to uninstall the NOS via ONIE.	
Additional Comments	

Test Information	Test Result
Test 3.1.1: Return loss for 10G Passive Cable	
<i>Purpose</i> : To verify that the return loss of the DUT is within the conformance limits provided by SFF-8431 Appendix E, Table 37.	PASS

Comments on Test Procedure

This test was completed using the standard procedure.

Comments on Test Results

The differential return loss observed did not violate the limits governed by SFF-8431 Appendix E.4, Table 37 for 10GBASE-CR passive cables:

$$12 - 2\sqrt{}, \qquad 0.01 \le < 4.1$$

$$11, \qquad 22 \ge \{ \\ 6.3 - 13 \log_{10} 5.5, \qquad 4.1 \le \le 11.1 \} ()$$

Additional Comments

Test Information	Test Result
Test 3.1.2: Insertion Loss for 10G Passive Cable	
<i>Purpose</i> : To verify that the insertion loss of the Cable under test is within the conformance limits provided by IEEE Std. 802.3-2012 Annex, Table 37.	PASS

Comments on Test Procedure

This test was completed using the standard procedure.

Comments on Test Results

The insertion loss of the cable under test does not violate the requirements passive cable assemblies.

$$3 \leq 21, \quad 12 \leq 17.04, \quad 5.15625$$

Additional Comments

Test Information	Test Result
Test 5.1.1: Output Rise and Fall Times for 10G Host	
Purpose: To verify that the Eye Mask Hit Ratio is within the conformance limits.	PASS
Comments on Test Procedure	J
The test was completed with the standard procedure.	
Comments on Test Results	
The device under test exhibited the expected behavior.	
Additional Comments	

Test Information	Test Result
Test 5.1.2: Transmitter Eye Mask on 10G Host Purpose: To verify that the Eye Mask Hit Ratio is within the conformance limits.	PASS
Comments on Test Procedure	
The test was completed with the standard procedure. Comments on Test Results The device under test exhibited the expected behavior.	
Additional Comments	
	,

Test Information	Test Result
Test 5.1.3:	
Purpose: To verify that the Total Jitter (TJ) is within the conformance limit.	PASS
Comments on Test Procedure	
The test was completed with the standard procedure.	
Comments on Test Results	
The device under test exhibited the expected behavior.	
Additional Comments	

Test Information	Test Result
Test 5.1.4: Input and Output Return Loss on 10G Host	
Purpose: To verify that the differential input and output return loss of the DUT is within conformance limits.	PASS
Comments on Test Procedure	
The test was completed with the standard procedure.	
Comments on Test Results	
The device under test exhibited the expected behavior.	
Additional Comments	
See Appendix B	, and the second se

Interoperability Test Results	
Test Information	Test Result
Test 2.1: Physical Compatibility with Supporting Devices	
<i>Purpose</i> : To verify that the mechanical form factor is compatible with devices for interoperability purposes.	PASS
Comments on Test Procedure	'
The test was completed with the standard procedure. Comments on Test Results	
Part A: The MUT/CUT was able to be inserted into the Host. Part B: This test is not applicable to Cable Assemblies. Part C: The MUT/CUT was able to be removed from the Host.	
Additional Comments	

Test Information	Test Result
Test 2.2:	PASS
Purpose: To verify that the MUT/CUT is manageable via the Host complex.	
Comments on Test Procedure	
The test was completed with the standard procedure.	
Comments on Test Results	
Part B: The EEPROM data of the MUT/CUT was readable. The serial number and veriform the EEPROM data matches the serial number and vendor information of Additional Comments	
Additional Comments	

Test Information	Test Result
Test 2.3:	
<i>Purpose</i> : To verify that the MUT/CUT (active optical cable only) supports diagnostic functions via the Host complex.	PASS
Comments on Test Procedure	
The test was completed with the standard procedure. Comments on Test Results	
Part A: The MUT/CUT supports diagnostic monitoring and the diagnostic information from readable by the NOS.	om the EEPROM was
Additional Comments	

Test Information	Test Result
Test 6.1: Establish Baseline Performance Analysis	D 4 G G
<i>Purpose</i> : To establish a baseline performance analysis of the HUT.	PASS
Comments on Test Procedure	d «

This test was completed using a modified procedure. Because of a lack of 40G Golden Modules, the Host was baselined using each 40G MUT/CUT.

Comments on Test Results

The baseline performance of the Host was determined to be 90% line rate. All proceeding tests in Group 6 were conducted using this line rate.

Additional Comments

Test Information	Test Result
Test 6.2:	
Purpose: To determine if the MUT/CUT, HUT and LP establish a link while varying the	PASS
power up sequence.	

Comments on Test Procedure

This test was completed using the standard procedure.

Comments on Test Results

- **Part A:** The Host and Link Partner were able to establish a valid link with this MUT/CUT while fully powered and operational.
- **Part B:** The Host and Link Partner were able to establish a valid link with this MUT/CUT when the Link Partner was powered on after the Host.
- **Part C:** The Host and Link Partner were able to establish a valid link with this MUT/CUT when the Host was powered on after the Link Partner.

Additional Comments

Test Information	Test Result
Test 6.3:Packet Error Rate Estimation	
<i>Purpose</i> : To determine if a Host can exchange packets with a Module or Cable Assembly such that a bit error rate of 10 ⁻¹² is achieved	PASS
Comments on Test Procedure	-9
This test was completed using the standard procedure.	
Comments on Test Results	
Part A: All 247,000,000 frames transmitted by TS1 were received by TS2. Part B: All 10,506,539,320 frames transmitted by TS1 were received by TS2.	
Additional Comments	

Test Information	Test Result	
Test 6.4: Packet Loss/Stress Test Purpose: To verify that no obvious buffer management problems occur when di large volume of traffic at the Host and Module/Cable Assembly combination.	recting a PASS	
Comments on Test Procedure		
This test was completed using the standard procedure.		
Comments on Test Results	1 (4)	
Parts A-D: All 1,000,000,000 64-byte frames transmitted by TS1 were received by TS2. All 1,000,000,000 1518-byte frames transmitted by TS1 were received by TS2.		
Additional Comments		

Appendix A: EEPROM Data

```
10Gtek QSFP28 module
Part Number: AOC-Q4Q4- 010 Serial Number: WTQ17100003
10Gtek WTQ17100003 EEPROMdecode 20171205170058.txt
SERIAL ID Keys:
BR NOMINAL: 25750
CONNECTOR: 35
CU ATTENUATE 2 5: 0
CU ATTENUATE 5 0: 0
DEVICE TECH: 0x0
ENCODING: 5
EXTENDED MODULE: 0x1f
EXT IDENTIFIER: 0
EXT RATE COMPLY: 0
IDENTIFIER: 17
LENGTH OM1 62 5UM: 0
LENGTH OM2 50UM: 0
LENGTH OM3 50UM: 0
LENGTH OM4 OR CU: 20
LENGTH SMF KM: 0
MAX CASE TEMP: 70
SPEC COMPLIANCE: 0x80 0x0 0x0 0x0 0x0 0x0 0x0 0x0
VENDOR NAME: 10Gtek
VENDOR OUI: 0x0 0x0 0x0
VENDOR PN: AOC-Q4Q4- 010
VENDOR REV: 01
WAVELENGTH: 0.0
WAVELEN TOLERANCE: 0.0
I2C Address A0h, bytes 0-127, in hex
0000x: 110700ff 0f360050 00555500 00000000
0010x: 00000000 00001600 00005118 00000000
0020x: 00000001 00010001 00010c1c 0c1c0c1c
0030x: 0c1c2620 26202620 26200000 00000000
0040x: 00000000 00000000 00000000 00000000
0050x: 00000000 00000000 00000000 00000000
0060x: 00000000 00000000 00000000 00000000
0070x: 00000000 00000000 00000000 00000000
I2C Address A0h, page 0, bytes 128-255, in hex
0000x: 11002380 00000000 00000005 ff000000
0010x: 00000a00 31304774 656b2020 20202020
0020x: 20202020 1f000000 414f432d 51345134
0030x: 2d303130 20202020 30310000 000046fc
0040x: 01000000 57545131 37313030 30303320
0050x: 20202020 31373130 30312020 08006702
0060x: 00000000 00000000 00000000 00000000
0070x: 00000000 00000000 00000000 00000000
```

Appendix B: Pluggable Module / Cable Electrical Data

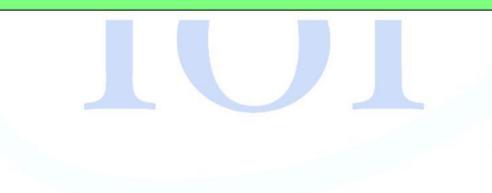
Optics Self-Test Report - Port 1: 100GigE Layer 2 Traffic Term

Generated by Viavi 5800-100G

Optics Self-Test

Customer Name	**
Technician ID	
Test Location	***
Work Order	**
Comments/Notes	**:

Overall Test Result: Pass



Appendix C: Host Electrical Data



